## OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

## **Application Data Sheet**

Inventor

**Applicant Information** 

Applicant Authority type::

Primary Citizenship Country:: Japan

Status:: Full Capacity

Given Name:: Eiichi

Family Name:: TAKAHASHI

City of Residence:: Tsukuba-shi

Country of Residence:: Japan

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan

Status:: Full Capacity

Given Name:: Yuji
Family Name:: KASAI

City of Residence:: Tsukuba-shi

Country of Residence:: Japan

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan

Status:: Full Capacity

Given Name:: Tetsuya
Family Name:: HIGUCHI

City of Residence:: Tsukuba-shi

Country of Residence:: Japan

**Correspondence Information** 

Correspondence Customer Number:: 25944

**Application Information** 

Application Type:: National Phase

Subject Matter:: Utility
CD-ROM or CD-R:: None

Title::

DIGITAL CIRCUIT HAVING DELAY

CIRCUIT FOR ADJUSTMENT OF CLOCK

SIGNAL TIMING

**Attomey Docket Number::** 

122358

Suggested Drawing Figure::

1

**Total Drawing Sheets::** 

19

Small Entity::

No

## **Representative Information**

Attorneys associated with Customer No. 25944.

Domestic Priority Information			
Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is a	National Stage of	PCT/JP03/08648	07/08/03
Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-200467	07/09/02	Yes
Assignee Information			
Assignee Name::		National Institute of Advanced Industrial Science and Technology	
Street of mailing address::		3-1, Kasumigaseki 1-chome, Chiyoda-ku	
City of mailing address::		Tokyo	
Country of mailing address::		Japan	
Postal or Zip Code of mailing address::		100-8921	